WHAT IS CLAIMED IS:

- 1. A semiconductor device, comprising:
- a ¹⁰B diffusion layer containing an isotope ¹⁰B of boron introduced therein;
 a pn junction for detecting an α-ray generated in said ¹⁰B diffusion layer; and
 an analytic circuit for analyzing electric charge generated in said pn junction,
 wherein

said $^{10}\mathrm{B}$ diffusion layer, said pn junction, and said analytic circuit are provided on a single semiconductor chip.

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- 2. The semiconductor device according to claim 1, wherein a p-type diffusion layer for defining said pn junction is said ¹⁰B diffusion layer.
- 3. The semiconductor device according to claim 2, wherein said ¹⁰B diffusion layer is provide in a periphery of an upper surface of said semiconductor chip, and

an n-type diffusion layer is provided under said ¹⁰B diffusion layer, said n-type diffusion layer defining said pn junction together with said ¹⁰B diffusion layer.

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4. The semiconductor device according to claim 1, wherein said analytic circuit is arranged farther from said ¹⁰B diffusion layer than said pn junction.